

Notice of Allowability	Application No.	Applicant(s)	
	10/629,592	KASAI, TOSHIYUKI	
	Examiner Nitin Patel	Art Unit 2629	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. This communication is responsive to 8/17/2007.
2. The allowed claim(s) is/are 1,3-7,9-13 Now renumbered 1-11 respectively.
3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a) All b) Some* c) None of the:

1. Certified copies of the priority documents have been received.
2. Certified copies of the priority documents have been received in Application No. _____.
3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) hereto or 2) to Paper No./Mail Date _____.
 - (b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

<ol style="list-style-type: none"> 1. <input type="checkbox"/> Notice of References Cited (PTO-892) 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) 3. <input checked="" type="checkbox"/> Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date <u>10/28/2003</u> 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit of Biological Material 	<ol style="list-style-type: none"> 5. <input type="checkbox"/> Notice of Informal Patent Application 6. <input type="checkbox"/> Interview Summary (PTO-413), Paper No./Mail Date _____. 7. <input type="checkbox"/> Examiner's Amendment/Comment 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance 9. <input type="checkbox"/> Other _____.
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NITIN I. PATEL
PRIMARY EXAMINER

REASON FOR ALLOWANCE

1. Claims 1,3-7,9-13 are allowed. Claims 2,8,14,15 have been cancelled.
2. The following is an examiner's statement of reason for allowance:

The prior art fails to teach or suggest an electronic circuit, comprising: a shift circuit that shifts j-bit digital data (j is a natural number) to be converted into k-bit digital data (k is a natural number); and a correction circuit that is electrically coupled to the shift circuit, the correction circuit continuously changes the k-bit digital data that is obtained by the shift circuit in accordance with the change of the j-bit data, the k-bit digital data being extended digital data which is larger than the j-bit digital data; and the shift circuit classifying a range of the j-bit digital data into a plurality of groups and shifting the digital data of each group by a predetermined number of bits in accordance with each group to convert it into the k-bit digital data as claimed in claim 1.

The prior art fails to teach or suggest an electro-optical device, comprising: a control circuit that outputs j-bit luminance gray scale data (j is a natural number); a driving circuit that generates analog driving signals based on the j-bit luminance gray scale data; and a pixel circuit that drives current driven elements based on the analog driving signals, the driving circuit including: a shift circuit that shifts the j-bit luminance gray scale data to convert the data into k-bit digital data (k is a natural number); a correction circuit that is electrically coupled to the shift circuit, the correction circuit continuously changing the k-bit digital data that is obtained by the shift circuit in accordance with the change of the j-bit luminance gray scale data, the k-bit digital data being extended digital data that is larger than the j-bit luminance gray scale data; and

the shift circuit classifying a range of the j-bit digital data into a plurality of groups and shifting the digital data of each group by a predetermined number of bits in accordance with each group to convert the digital data into the k-bit digital data as claimed in claim

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3. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nitin Patel whose telephone number is 571-272-7677. The examiner can normally be reached on 8:00-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bipin H. Shalwala can be reached on 571-272-7681. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Art Unit: 2629

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.



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PRIMARY EXAMINER